

AMENDMENTS TO THE ABSTRACT

A reactor (5) having a predetermined small capacity is connected to a rectifier, (2), and a capacitor (6) having a predetermined small capacity which is connected between DC bus lines of the inverter (3). A PN voltage corrector (10) calculates a ratio of the DC voltage detection value of the inverter obtained by the PN voltage detector (9) to a predetermined DC voltage reference value of the inverter (3) to thereby generate a PN voltage correction factor. A beat amount corrector (16) calculates a fluctuation amount of the motor current from the motor current detection value obtained by the motor current detector (15) and generates a reverse phase component of the motor current fluctuation amount. Thus, a small, light and low-cost inverter controller can be implemented.